

**AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions and listings of claims in the application.

**Listing of Claims:**

Please amend the claims as follows:

Claim 1 (Currently Amended): An in-plane switching mode liquid crystal display device comprising:  
first and second substrates having an array region and a sealant region along a periphery of the array region, wherein the second substrate includes gate lines and data lines arranged horizontally and vertically to define a pixel region, a thin film transistor adjacent each crossing of the gate and data lines, and a gate pad and a data pad at an end of the gate and data lines;  
a sealant in the sealant region attaching the first and second substrates, wherein the sealant is located over the gate and data pads;  
a metallic black matrix formed in the sealant region that extends into the array region of the first substrate;  
a color filter on the metallic black matrix extending into the array region from the sealant region;  
an organic layer on the color filter; and  
a liquid crystal layer between the first and second substrates.

Claim 2 (Original): The device of claim 1, wherein the metallic black matrix is one of Cr and CrO<sub>x</sub>.

Claim 3 (Original): The device of claim 1, wherein the organic layer is formed in the array region.

Claim 4 (Original): The device of claim 3, wherein the organic layer is in direct contact with the metallic black matrix.

Claim 5 (Original): The device of claim 1, wherein the organic layer is formed in the array region and in the sealant region.

Claim 6 (Original): The device of claim 5, wherein the organic layer is in direct contact with the sealant.

Claim 7 (Currently Amended): The device of claim 1, wherein the second substrate further comprises:

~~gate lines and data lines arranged vertically and horizontally to define a pixel region;~~

~~a thin film transistor adjacent each crossing of the gate and data lines;~~

~~a gate pad and a data pad at an end of the gate and data lines; and~~

a common electrode and a pixel electrode in the pixel region.

Claim 8 (Currently Amended): A method for fabricating an in-plane switching mode liquid crystal display device, comprising:

providing first and second substrates having a sealant region and an array region, wherein the second substrate includes gate lines and data lines arranged horizontally and vertically to define a pixel region, a thin film transistor adjacent each crossing of the gate and data lines, and a gate pad and a data pad at an end of the gate and data lines;

forming a metallic black matrix in the sealant region that extends into the array region of the first substrate;

forming a color filter on the metallic black matrix extending into the array region from the sealant region;

forming an organic layer on the color filter;

forming a sealant over the gate and data pads of [[in]] the sealant region; and

attaching the first and second substrates by the sealant.

Claim 9 (Original): The method of claim 8, wherein the metallic black matrix is one of Cr and CrO<sub>x</sub>.

Claim 10 (Original): The method of claim 8, wherein the organic layer is formed in the array region.

Claim 11 (Original): The method of claim 8, wherein the organic layer is formed in the sealant region and the array region.

Claim 12 (Currently Amended): The method of claim 8, further comprising: forming ~~a thin film transistor~~, a pixel electrode and a common electrode on the second substrate.

Claim 13 (Original): The method of claim 8, further comprising: forming a liquid crystal layer between the first and second substrates.

Claim 14 (Previously Presented): The device of claim 1, wherein the black matrix extends over at least one thin film transistor in the array region.

Claim 15 (Previously Presented): The device of claim 8, wherein the black matrix extends over at least one thin film transistor in the array region.